



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IPW

Docket No.: CYPR-PM01011

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.

Date of Deposit:	12/30/04	Name of Person Making the Deposit:	Judy Davenport	Signature of the Person Making the Deposit:	
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Inventor(s): Bo Soon CHANG

Serial No.: 10/085,752

Group Art Unit: 2812

Filed: 02/27/02

Examiner: STEVENSON, Andre C.

Confirmation No: 8457

Title: A METHOD AND SYSTEM FOR CONTROLLING THE PROCESSING OF AN INTEGRATED CIRCUIT CHIP ASSEMBLY LINE USING A CENTRAL COMPUTER SYSTEM AND A COMMON COMMUNICATION PROTOCOL

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

TRANSMITTAL OF FORMAL DRAWINGS

In response to Drawing Informalities

attached please find:

☒ (a) the formal drawings for this application  
Number of Sheets 15

Each sheet of drawing indicates the identifying indicia suggested in § 1.84(c) on the reverse side of the drawing

☐ (b) a copy of the NOTICE OF INFORMAL DRAWINGS

Please direct all correspondence concerning the above-identified application to the following address:

**WAGNER, MURABITO & HAO LLP**  
Two North Market Street, Third Floor  
San Jose, California 95113  
(408) 938-9060

Respectfully submitted,

Date: 12/30/2004

By:   
Anthony C. Murabito  
Reg. No. 35,295